

**Korea–France Collaboration
Meeting on LLRF Noise
Reduction using FPGA and
Machine Learning**

Report of Contributions

Contribution ID: 1

Type: **not specified**

Introduction of the Korean Collaboration Group

Thursday, 29 January 2026 16:30 (10 minutes)

Contribution ID: 2

Type: **not specified**

Introduction of the French Collaboration Group

Thursday, 29 January 2026 16:40 (10 minutes)

Contribution ID: 3

Type: **not specified**

Korea-4GSR Project Overview and Current Progress in LLRF Development

Thursday, 29 January 2026 16:50 (10 minutes)

Presenter: PARK, Chong Shik (Korea University)

Contribution ID: 4

Type: **not specified**

R&D on Machine-Learning-Based LLRF Control at GANIL

Thursday, 29 January 2026 17:10 (30 minutes)

Presenter: LASSALLE, Charly (GANIL)

Contribution ID: 5

Type: **not specified**

Open Discussion and Future Collaboration

Thursday, 29 January 2026 17:40 (20 minutes)

Contribution ID: 6

Type: **not specified**

FPGA implementation study for a U.S. tokamak

Thursday, 29 January 2026 17:00 (10 minutes)

Presenter: JOUNG, Semin (University of Wisconsin)